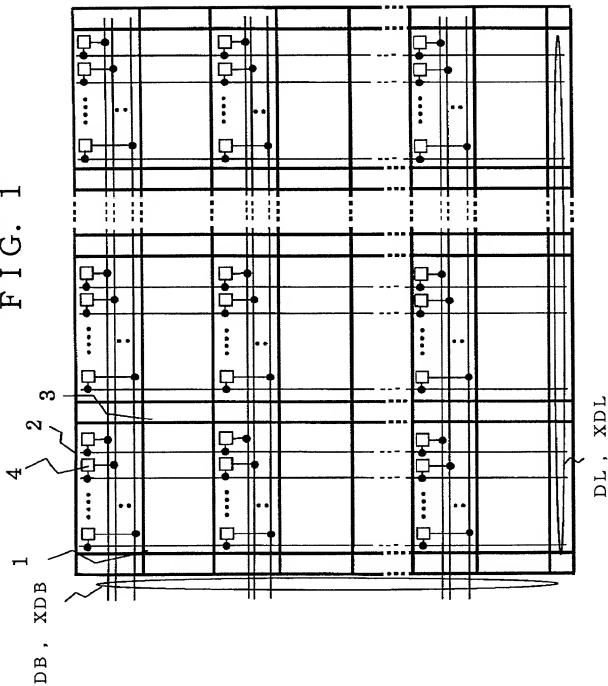


FIG. 1



T05250-EB249860

FIG. 2

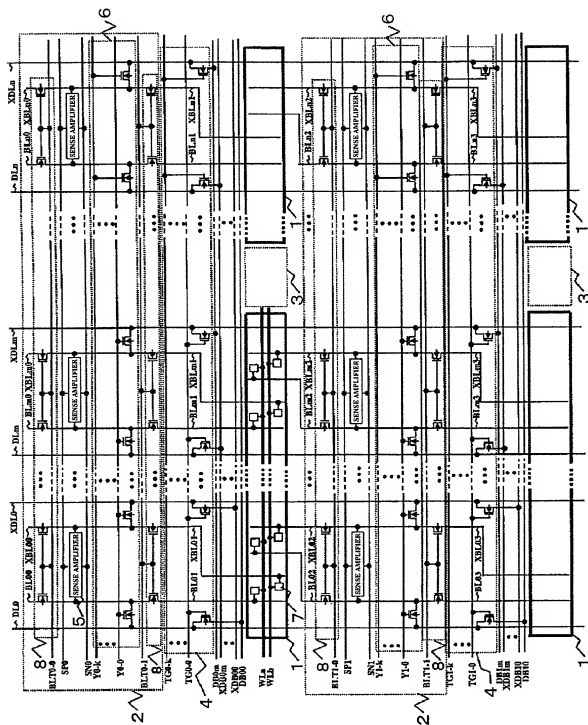
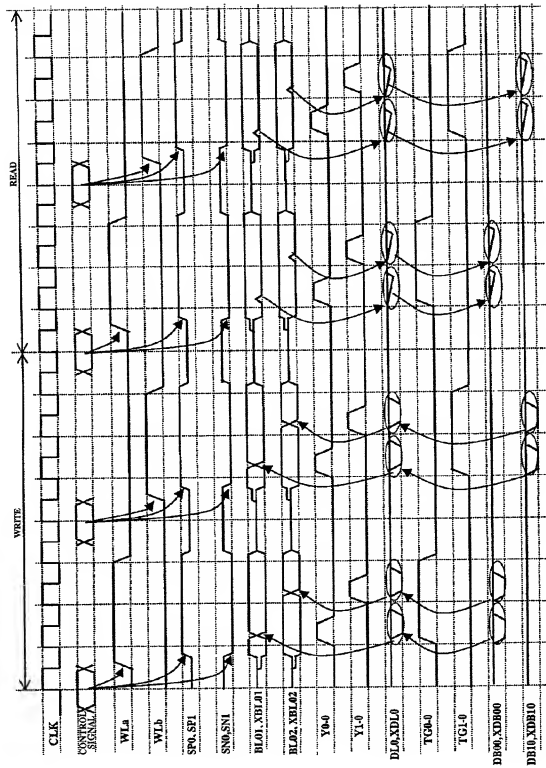
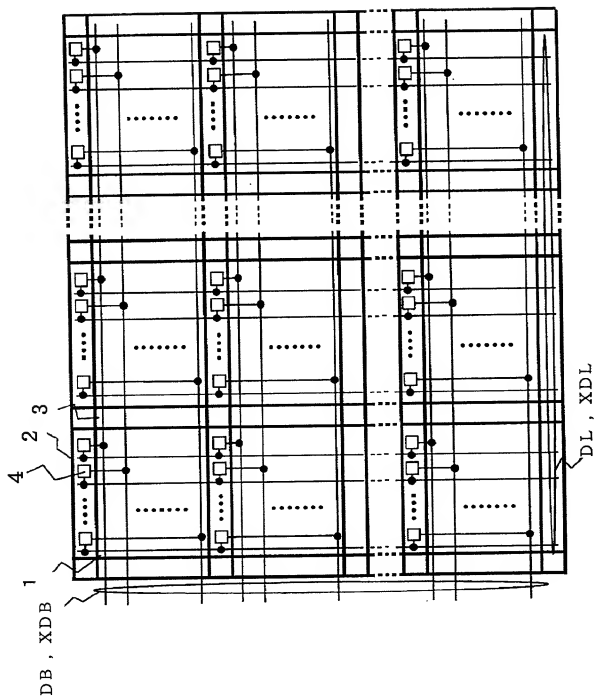


FIG. 3



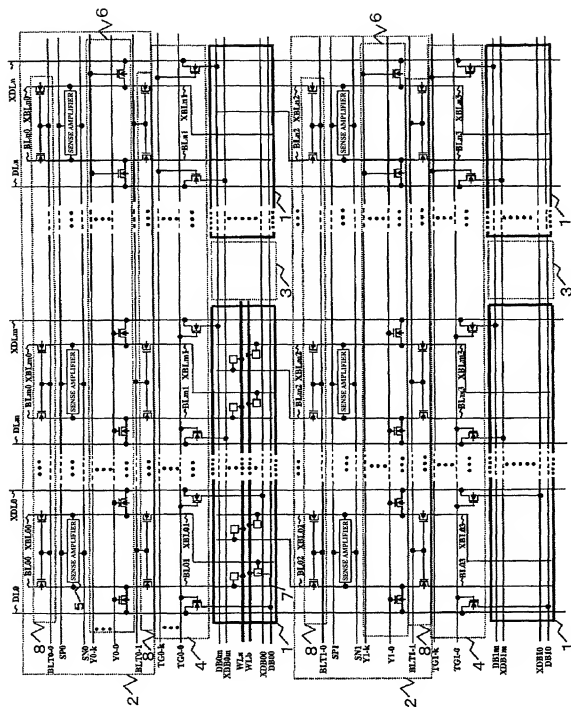
105250" E0249860

FIG. 4



T05250*88249860

FIG. 5



TDS250" 03219860

FIG. 7

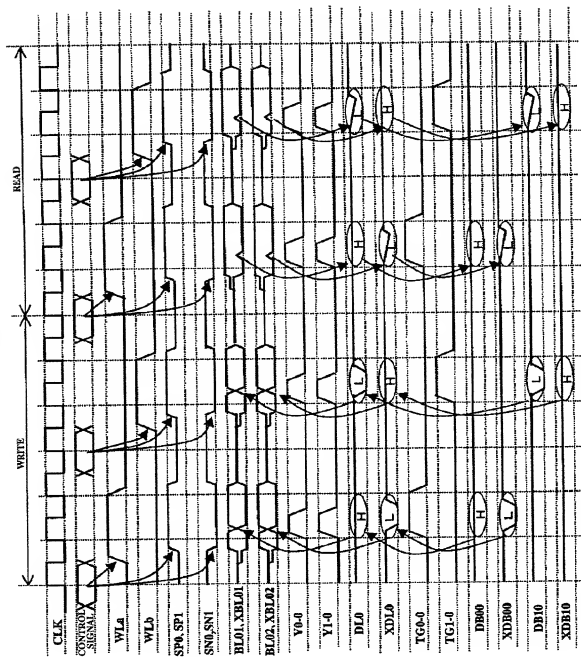
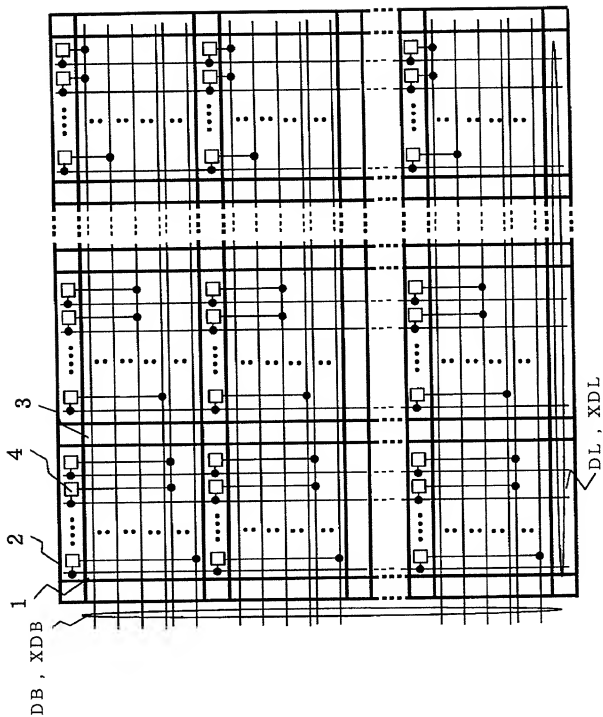


FIG. 8



705250" E2219860

FIG. 10

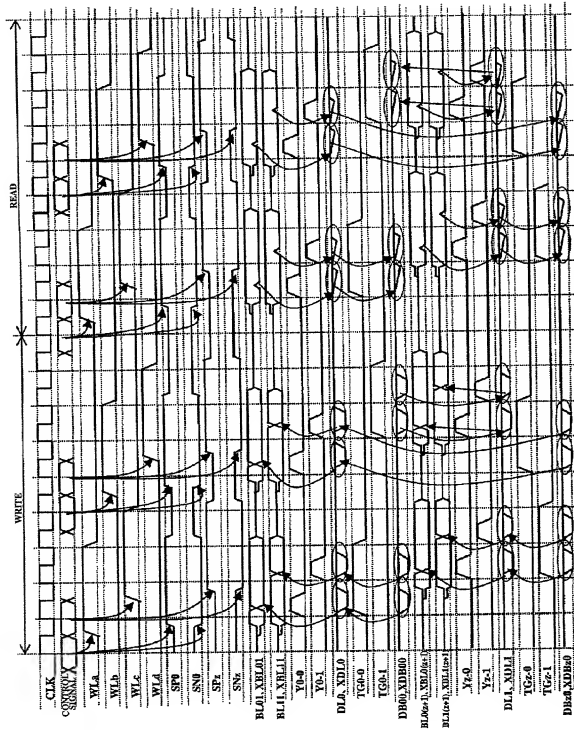


FIG. 11

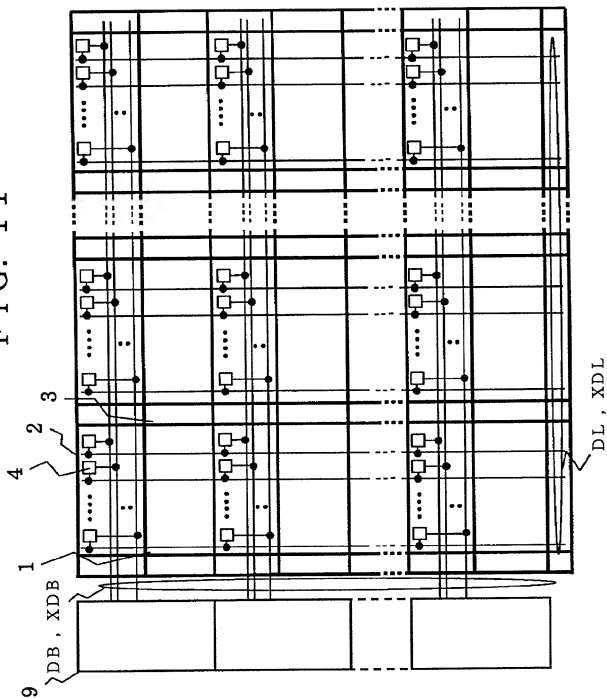
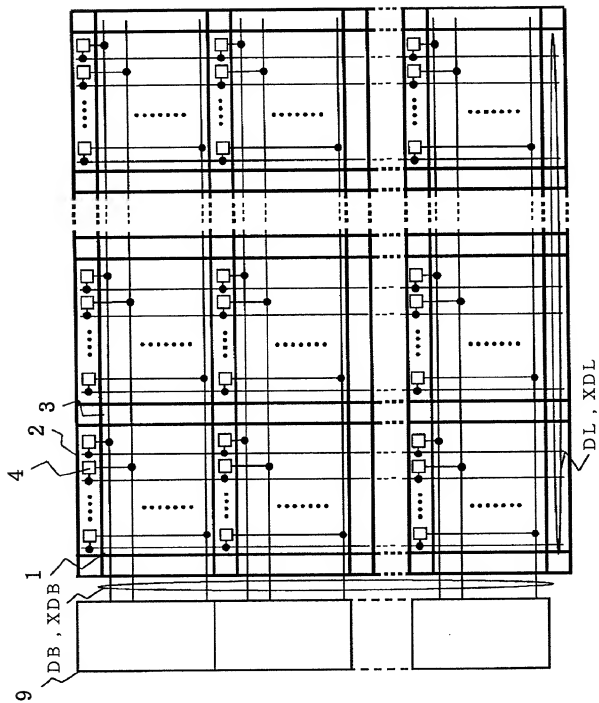


FIG. 12



105250" 2024900

FIG. 13

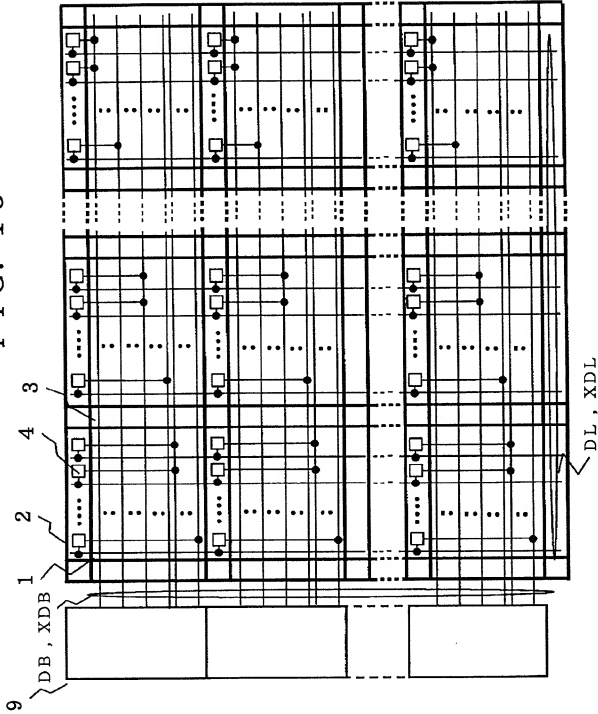
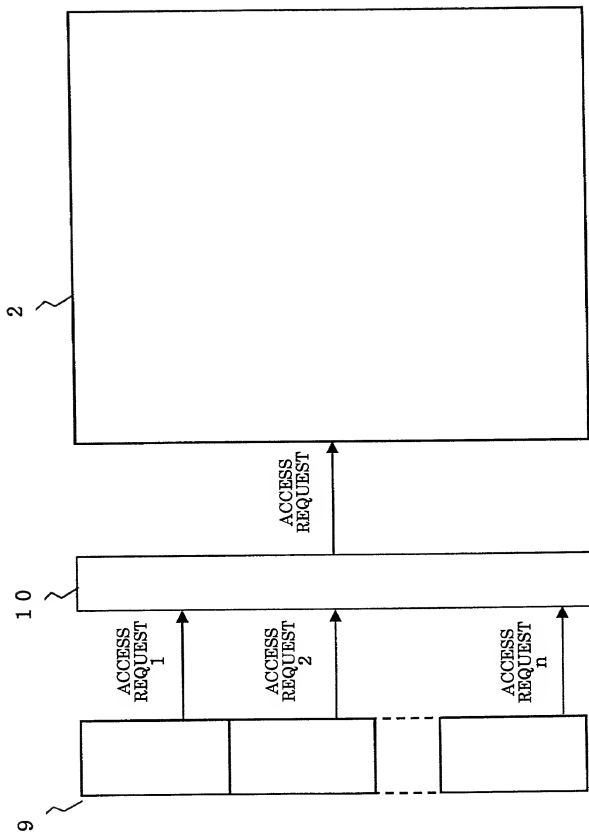


FIG. 14



T05250*8249860

FIG. 15

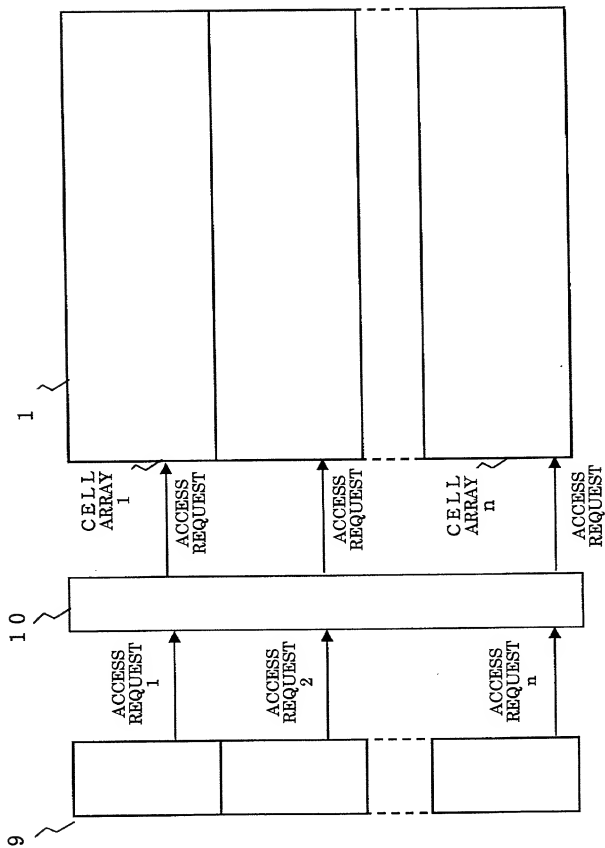


FIG. 16

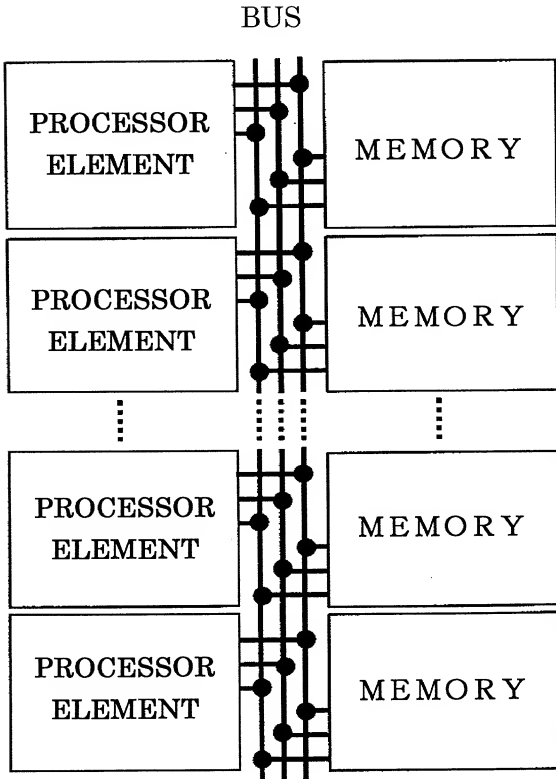
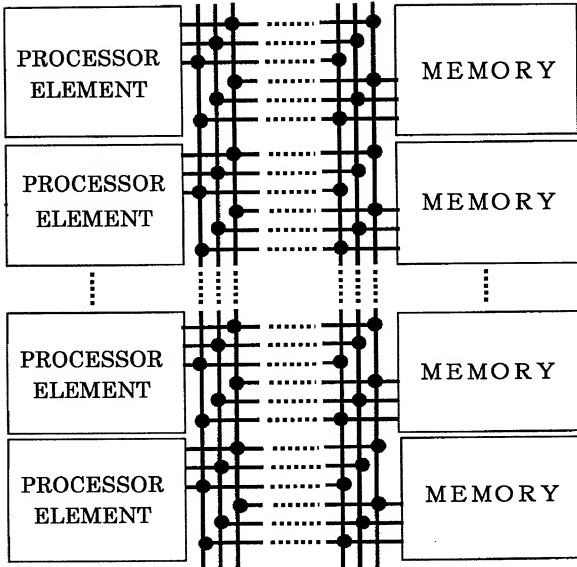
00661203.052501
105250.0024980

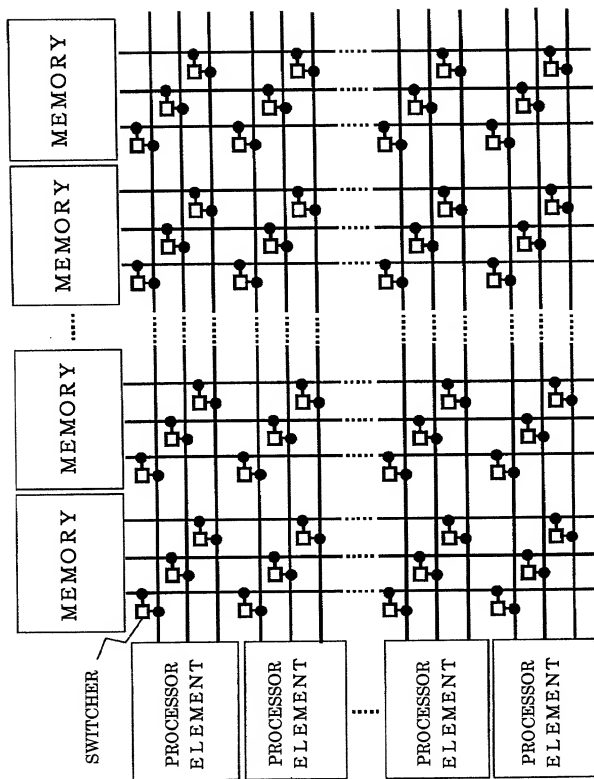
FIG. 17



09864283-052504

T05250" 88249860

FIG. 18



REFERENCE NUMERALS IN THE DRAWINGS

- 1 CELL ARRAY
- 2 SENSE AMPLIFIER ROW
- 3 SUB-WORD DECODER ARRAY
- 4, 6 SWITCHER
- 5 SENSE AMPLIFIER
- 7 MEMRY CELL
- 8 SELECTION GATE
- 9 PROCESSOR ELEMENT
- 10 ARBITER CIRCUIT
- BL,XBL BIT LINE PAIRS
- DL,XDL FIRST DATA LINE PAIRS
- DB,XDB SECOND DATA LINE PAIRS
- Y,TG GATE SIGNAL
- SP,SN SENSE AMPLIFIER STARTING SIGNAL
- WL WORD LINE

105250 00000000